Andrés Cavieres joins the DiA as an assistant professor this fall. His areas of expertise are Design Computation, Knowledge Representation in Design and Building Information Modeling. He holds a Professional degree in Architecture, from the University of Chile. Before coming to United States, Andres was a lecturer at Universidad de Chile, where he taught Computer-Aided Design and Parametric Modeling, as well as first and fifth year design studios at Universidad de Chile. He is a Ph. D. Candidate in Architecture at the Georgia Institute of Technology.

During his time at Georgia Tech, Andres was part of the Digital Fabrication Lab (DFL), teaching courses on digital fabrication and parametric design. He is also part of the Digital Building Lab (DBL), developing research on Building Information Modeling for the masonry industry (BIM-M initiative). He also participated in a large interdisciplinary research project funded by the DoE on photovoltaic systems, leading several inventions with patent pending. His work has been published in journals and conference proceedings, and exhibited at the Biennale of Architecture in Sao Paulo, the Museum of Design of Atlanta (MODA); at the AIA New York's Center for Architecture. More recently, Andres was been awarded the King Student Medal for Excellence in Architectural + Environmental Research by the Architectural Research Centers Consortium – ARCC.