Micromachined Accelerometer

The MMA series of silicon capacitive, micromachined accelerometers features signal conditioning, a 4–pole low pass filter and temperature compensation. Zero–g offset full scale span and filter cut–off are factory set and require no external devices. A full system self–test capability verifies system functionality.

Features
• Integral Signal Conditioning
• Linear Output
• Ratiometric Performance
• 4th Order Bessel Filter Preserves Pulse Shape Integrity
• Calibrated Self–test
• Low Voltage Detect, Clock Monitor, and EPROM Parity Check Status
• Transducer Hermetically Sealed at Wafer Level for Superior Reliability
• Robust Design, High Shocks Survivability
• Two Packaging Options Available:
  1) Plastic DIP for Z Axis Sensing (MMA1201P)
  2) Wingback for X Axis Sensing (MMA2200W)

Typical Applications
• Vibration Monitoring and Recording
• Appliance Control
• Mechanical Bearing Monitoring
• Computer Hard Drive Protection
• Computer Mouse and Joysticks
• Virtual Reality Input Devices
• Sports Diagnostic Devices and Systems

Figure 1. Simplified Accelerometer Functional Block Diagram
MAXIMUM RATINGS
(Maximum ratings are the limits to which the device can be exposed without causing permanent damage.)

<table>
<thead>
<tr>
<th>Rating</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Powered Acceleration (all axes)</td>
<td>G$_{pd}$</td>
<td>500</td>
<td>g</td>
</tr>
<tr>
<td>Unpowered Acceleration (all axes)</td>
<td>G$_{upd}$</td>
<td>2000</td>
<td>g</td>
</tr>
<tr>
<td>Supply Voltage</td>
<td>V$_{DD}$</td>
<td>–0.3 to +7.0</td>
<td>V</td>
</tr>
<tr>
<td>Drop Test$^{(1)}$</td>
<td>D$_{drop}$</td>
<td>1.2</td>
<td>m</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>T$_{stg}$</td>
<td>–40 to +105</td>
<td>°C</td>
</tr>
</tbody>
</table>

NOTES:
1. Dropped onto concrete surface from any axis.

ELECTRO STATIC DISCHARGE (ESD)

**WARNING:** This device is sensitive to electrostatic discharge.

Although the Motorola accelerometers contain internal 2kV ESD protection circuitry, extra precaution must be taken by the user to protect the chip from ESD. A charge of over 2000 volts can accumulate on the human body or associated test equipment. A charge of this magnitude can alter the performance or cause failure of the chip. When handling the accelerometer, proper ESD precautions should be followed to avoid exposing the device to discharges which may be detrimental to its performance.
## Operating Characteristics

(Unless otherwise noted: \(0^\circ\text{C} \leq T_A \leq +85^\circ\text{C}, 4.75 \leq V_{DD} \leq 5.25, \text{Acceleration} = 0\text{g}, \text{Loaded output}\))

### Characteristic | Symbol | Min | Typ | Max | Unit
--- | --- | --- | --- | --- | ---
Operating Range | | | | | |
Supply Voltage | \(V_{DD}\) | 4.75 | 5.00 | 5.25 | V
Supply Current | \(I_{DD}\) | 4.0 | 5.0 | 6.0 | mA
Operating Temperature Range | \(T_A\) | –40 | –40 | +85 | °C
Acceleration Range | \(g_{FS}\) | | 38 | | g

| Characteristic | Symbol | Min | Typ | Max | Unit |
--- | --- | --- | --- | --- | ---
Output Signal | Zero g \((V_{DD} = 5.0\text{V})\) | \(V_{OFF}\) | 2.2 | 2.5 | V
Zero g | \(V_{OFF,V}\) | 0.44 \(V_{DD}\) | 0.50 \(V_{DD}\) | 0.56 \(V_{DD}\) | V
Sensitivity \((T_A = 25^\circ\text{C}, V_{DD} = 5.0\text{V})\) | \(S\) | 47.5 | 50 | 52.5 | mV/g
Sensitivity \((V_{DD} = 5.0\text{V})\) | \(S_{V}\) | 9.3 | 10 | 10.7 | mV/g/V
Bandwidth Response Nonlinearity | \(f_{-3\text{dB}}\) | 360 | 400 | 440 | Hz
| \(N_{L\text{OUT}}\) | –1.0 | | | +1.0 | % FSO
Noise | RMS \((.01–1\text{kHz})\) | \(n_{\text{RMS}}\) | | | 3.5 | mVrms
| Power Spectral Density | \(n_{\text{PSD}}\) | | | | | µV/(Hz\(1/2\))
| Clock Noise (without RC load on output) | \(n_{\text{CLK}}\) | | | | | mVpk
Self–Test | Output Response | \(g_{ST}\) | 20 | | 30 | g
| Output Low | \(V_{IL}\) | | | | \(0.3\times V_{DD}\) | V
| Output High | \(V_{IH}\) | 0.7 \(V_{DD}\) | | | \(V_{DD}\) | V
| Input Loading | \(I_{IN}\) | –30 | –110 | –300 | µA
| Response Time | \(t_{ST}\) | | 2.0 | | 10 | ms
Status | Output Low \((I_{load} = 100\mu\text{A})\) | \(V_{OL}\) | | | 0.4 | V
| Output High \((I_{load} = 100\mu\text{A})\) | \(V_{OH}\) | \(V_{DD} – 0.8\) | | | | V
Minimum Supply Voltage (LVD Trip) | | \(V_{LVD}\) | 2.7 | 3.25 | 4.0 | V
Clock Monitor Fail Detection Frequency | | \(f_{\text{min}}\) | 50 | | 260 | kHz
Output Stage Performance Electrical Saturation Recovery Time | \(t_{\text{DELAY}}\) | | | | | ms
| Full Scale Output Range \((I_{OUT} = 200\mu\text{A})\) | \(V_{FSO}\) | 0.3 | | | \(V_{DD} – 0.3\) | V
| Capacitive Load Drive | \(C_{L}\) | | | | 100 | pF
| Output Impedance | | \(Z_{O}\) | 300 | | | Ω
Mechanical Characteristics | Transverse Sensitivity \((11)\) | \(V_{ZX,YX}\) | | | | % FSO
| Package Resonance | \(f_{\text{PKG}}\) | | | | 10 | kHz

### Notes:
1. For a loaded output the measurements are observed after an RC filter consisting of a 1 kΩ resistor and a 0.01 µF capacitor to ground.
2. These limits define the range of operation for which the part will meet specification.
3. Within the supply range of 4.75 and 5.25 volts, the device operates as a fully calibrated linear accelerometer. Beyond these supply limits the device may operate as a linear device but is not guaranteed to be in calibration.
4. The device can measure both + and – acceleration. With no input acceleration the output is at midsupply. For positive acceleration the output will increase above \(V_{DD}/2\) and for negative acceleration the output will decrease below \(V_{DD}/2\).
5. The device is calibrated at 20g.
6. At clock frequency \(= 70\text{kHz}\).
7. The digital input pin has an internal pull–down current source to prevent inadvertent self test initiation due to external board level leakages.
8. Time for the output to reach 90% of its final value after a self–test is initiated.
9. Time for amplifiers to recover after an acceleration signal causing them to saturate.
10. Preserves phase margin (60°) to guarantee output amplifier stability.
11. A measure of the device’s ability to reject an acceleration applied 90° from the true axis of sensitivity.
12. The Status pin output is not valid following power–up until at least one rising edge has been applied to the self–test pin. The Status pin is high when the self–test input is high.
13. The Status pin output latches high if a Low Voltage Detection or Clock Frequency failure occurs, or if the EPROM parity changes to odd. The Status pin can be reset by a rising edge on self–test, unless a fault condition continues to exist.
PRINCIPLE OF OPERATION

The Motorola accelerometer is a surface–micromachined integrated–circuit accelerometer.

The device consists of a surface micromachined capacitive sensing cell (g–cell) and a CMOS signal conditioning ASIC contained in a single integrated circuit package. The sensing element is sealed hermetically at the wafer level using a bulk micromachined “cap” wafer.

The g–cell is a mechanical structure formed from semiconductor materials (polysilicon) using semiconductor processes (masking and etching). It can be modeled as two stationary plates with a moveable plate in–between. The center plate can be deflected from its rest position by subjecting the system to an acceleration (Figure 2).

When the center plate deflects, the distance from it to one fixed plate will increase by the same amount that the distance to the other plate decreases. The change in distance is a measure of acceleration.

The g–cell plates form two back–to–back capacitors (Figure 3). As the center plate moves with acceleration, the distance between the plates changes and each capacitor’s value will change, \( C = \frac{A \varepsilon}{D} \). Where \( A \) is the area of the plate, \( \varepsilon \) is the dielectric constant, and \( D \) is the distance between the plates.

The CMOS ASIC uses switched capacitor techniques to measure the g–cell capacitors and extract the acceleration data from the difference between the two capacitors. The ASIC also signal conditions and filters (switched capacitor) the signal, providing a high level output voltage that is ratiometric and proportional to acceleration.

Filtering

The Motorola accelerometers contain an onboard 4–pole switched capacitor filter. A Bessel implementation is used because it provides a maximally flat delay response (linear phase) thus preserving pulse shape integrity. Because the filter is realized using switched capacitor techniques, there is no requirement for external passive components (resistors and capacitors) to set the cut–off frequency.

Self–Test

The sensor provides a self–test feature that allows the verification of the mechanical and electrical integrity of the accelerometer at any time before or after installation. This feature is critical in applications such as automotive airbag systems where system integrity must be ensured over the life of the vehicle. A fourth “plate” is used in the g–cell as a self–test plate. When the user applies a logic high input to the self–test pin, a calibrated potential is applied across the self–test plate and the moveable plate. The resulting electrostatic force \( F_e = \frac{1}{2} AV^2/d^2 \) causes the center plate to deflect. The resultant deflection is measured by the accelerometer’s control ASIC and a proportional output voltage results. This procedure assures that both the mechanical (g–cell) and electronic sections of the accelerometer are functioning.

Ratiometricity

Ratiometricity simply means that the output offset voltage and sensitivity will scale linearly with applied supply voltage. That is, as you increase supply voltage the sensitivity and offset increase linearly; as supply voltage decreases, offset and sensitivity decrease linearly. This is a key feature when interfacing to a microcontroller or an A/D converter because it provides system level cancellation of supply induced errors in the analog to digital conversion process.

Status

Motorola accelerometers include fault detection circuitry and a fault latch. The Status pin is an output from the fault latch, OR’d with self–test, and is set high whenever one (or more) of the following events occur:

- Supply voltage falls below the Low Voltage Detect (LVD) voltage threshold
- Clock oscillator falls below the clock monitor minimum frequency
- Parity of the EPROM bits becomes odd in number.

The fault latch can be reset by a rising edge on the self–test input pin, unless one (or more) of the fault conditions continues to exist.

BASIC CONNECTIONS

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Pin Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>—</td>
<td>Leave unconnected or connect to signal ground</td>
</tr>
<tr>
<td>2</td>
<td>ST</td>
<td>Logic input pin to initiate self test</td>
</tr>
<tr>
<td>3</td>
<td>VOUT</td>
<td>Output voltage</td>
</tr>
<tr>
<td>4</td>
<td>Status</td>
<td>Logic output pin to indicate fault</td>
</tr>
<tr>
<td>5</td>
<td>VSS</td>
<td>Signal ground</td>
</tr>
<tr>
<td>6</td>
<td>VDD</td>
<td>Supply voltage (5 V)</td>
</tr>
<tr>
<td>—</td>
<td>Wings</td>
<td>Support pins, internally connected to lead frame. Tie to VSS.</td>
</tr>
</tbody>
</table>
Pinout Description for the DIP Package

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Pin Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>—</td>
<td>Leave unconnected or connect to signal ground.</td>
</tr>
<tr>
<td>2 thru 3</td>
<td>—</td>
<td>No internal connection. Leave unconnected.</td>
</tr>
<tr>
<td>4</td>
<td>ST</td>
<td>Logic input pin to initiate self test.</td>
</tr>
<tr>
<td>5</td>
<td>VOUT</td>
<td>Output voltage</td>
</tr>
<tr>
<td>6</td>
<td>Status</td>
<td>Logic output pin to indicate fault.</td>
</tr>
<tr>
<td>7</td>
<td>VSS</td>
<td>Signal ground</td>
</tr>
<tr>
<td>8</td>
<td>VDD</td>
<td>Supply voltage (5 V)</td>
</tr>
<tr>
<td>9 thru 13</td>
<td>Trim Pins</td>
<td>Used for factory trim. Leave unconnected.</td>
</tr>
<tr>
<td>14 thru 16</td>
<td>—</td>
<td>No internal connection. Leave unconnected.</td>
</tr>
</tbody>
</table>

NOTES:
- Use a 0.1 μF capacitor on VDD to decouple the power source.
- Physical coupling distance of the accelerometer to the microcontroller should be minimal.
- Place a ground plane beneath the accelerometer to reduce noise, the ground plane should be attached to all of the open ended terminals shown in Figure 6.
- Use an RC filter of 1 kΩ and 0.01 μF on the output of the accelerometer to minimize clock noise (from the switched capacitor filter circuit).
- PCB layout of power and ground should not couple power supply noise.
- Accelerometer and microcontroller should not be a high current path.
- A/D sampling rate and any external power supply switching frequency should be selected such that they do not interfere with the internal accelerometer sampling frequency. This will prevent aliasing errors.
MMA1201P  MMA2200W

Positive Acceleration Sensing Direction

**DIP PACKAGE**

**WINGBACK PACKAGE**

* When positioned as shown, the Earth’s gravity will result in a positive 1g output

Drilling Patterns

**WB PACKAGE DRILLING PATTERN**

Measurement in inches

**ORDERING INFORMATION**

<table>
<thead>
<tr>
<th>Device</th>
<th>Temperature Range</th>
<th>Case No.</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MMA1201P</td>
<td>–40 to +85°C</td>
<td>Case 648C–04</td>
<td>Plastic DIP</td>
</tr>
<tr>
<td>MMA2200W</td>
<td>–40 to +85°C</td>
<td>Case 456–06</td>
<td>Plastic Wingback</td>
</tr>
</tbody>
</table>
PACKAGE DIMENSIONS

CASE 648C–04
ISSUE D
DIP PACKAGE

CASE 456–06
ISSUE H
WB PACKAGE

NOTES:
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
MOTOROLA Sensor Device Data

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